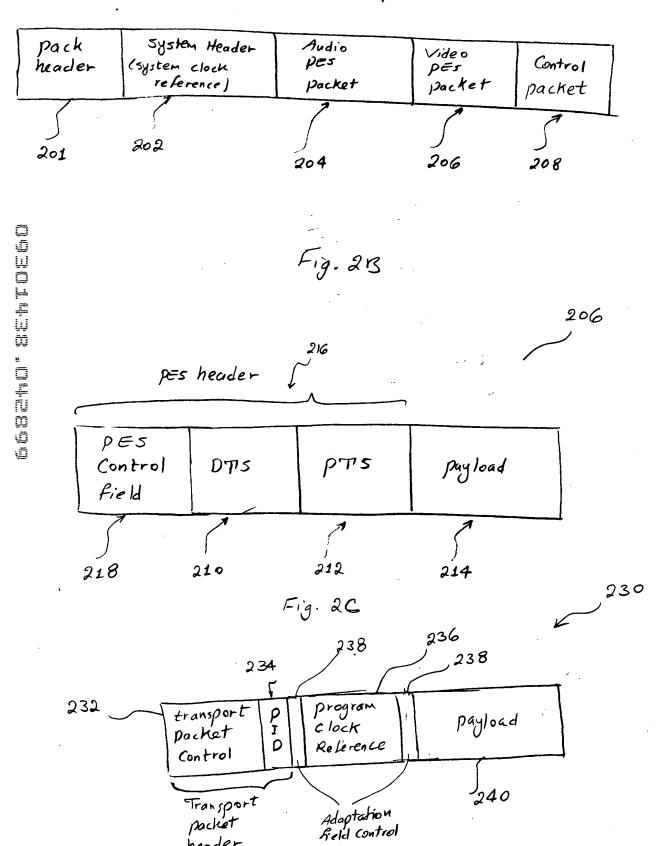


Fig. 2A 200



header

Format of a Task Definition Packet

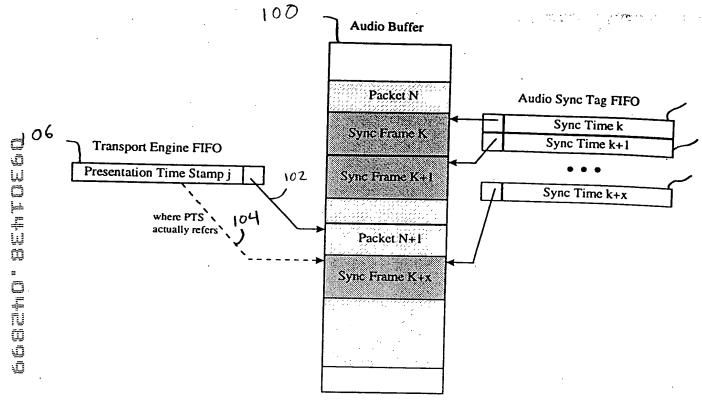
Word offset	Data [31:0]			
0	Address of picture to be decoded			
1	picture size bits			
2	picture Control and Fcode Register bits			
3-34	Quantizer Matrix Coefficients			
35-39	Reserved			

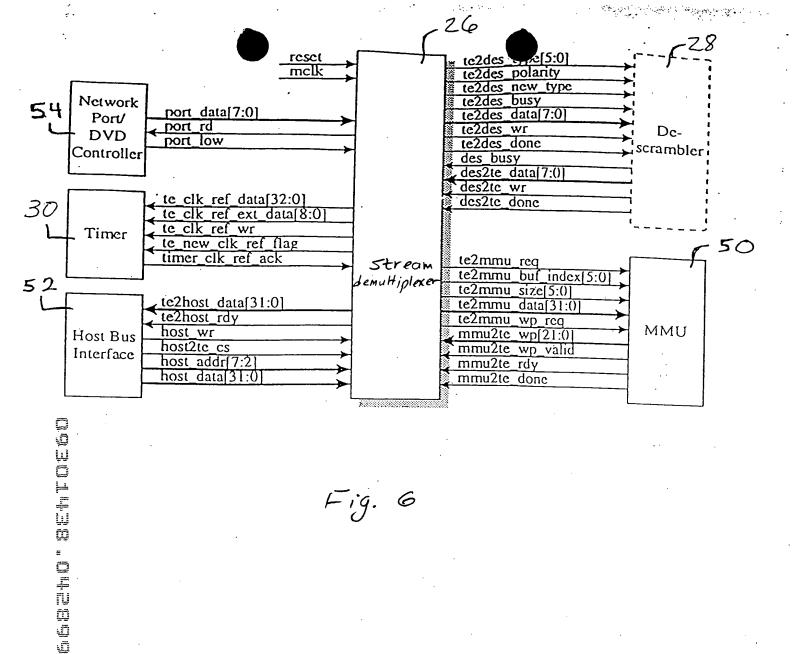
Fig. 3

Audio Sync Word Message Format

bits [31:20]	bits [19:10]	bits [9:0]
	SAMPRATE	NCHANS
	INBUF_LEVEL	LAYER
The state of the s	FRM_SIZE	BITRATE
Control of the Contro	OUTBU	JF WR PTR
28	INBU	F_RD_PTR
	FRM	NUMBER
Carrier Street	S	TATUS
	MSG_TYPE = 3	MSG ID = 0x100 - 0x1FF

Fig. 4





t	1		1	
N	31:6			Reserved
	6:5	2	DTS	Valid DTS present 00 = no DTS
			1 .	01 = DTS carried in PES
	·			10 = DTS carried in DTS_next_AU field of transport packet
ŀ		-		11 = reserved
	4:3	2	ERROR	Еггог Туре 00, 01 = по еггог
l ·	1		<u> </u>	10 = Transport Propagated Error
	1 .	l		11 = Transport Continuity Error
	2:0	3	EVENT	Event Type 000 = Video Start Code
				001 = Packet
				010 = Substream Section
	1.	1		011 = Table Section
				100-110 = reserved
	1			111 = mid-stream error
N+1	31:27	5	BUFFER	The number of the buffer containing the event
• • • •	26:22	5	OFFSET	Offset from address of the event, modulo buffer length
	21:0	22	ADDRESS	The byte address of the event in the buffer, minus offset
				modulo buffer length
N+2	31:0	32	DATA_LO	Event Data Low.
1				If EVENT is
		İ		000 - 10 : If DTS is
		1		0: bits [31:0] = reserved
		1		1 : bits [31:0] = DTS[32:1]
				011 :bits (31:0) = table header bytes 1, 2, 3, 4
1				100 - 111 : reserved
N+3	31:0	32	DATA_HI	Event Data High.
				If EVENT is
		1		000 - 01 : bits [30:0] = reserved. If DTS is
				0 : bits [31] = reserved
	<u> </u>			1 : bits [31] = DTS[0]
ĺ		ŀ		
				010: bits [23:0] = sub-stream header bytes 2, 3, 4 if Substream ID TYPE = 01 or 10.
				bits [7:1] = reserved
				If DTS is
		1		0 : bit [31] = reserved
	ŀ			1 : bit [31] = DTS[0]
		İ		011 : bits [31:0] = table header bytes 5, 6, 7, 8
				100 - 111: reserved
L				.1

			***************************************		(10)	0
•	PCI	DSI	Video	Audio	DSI	

Fig. 8

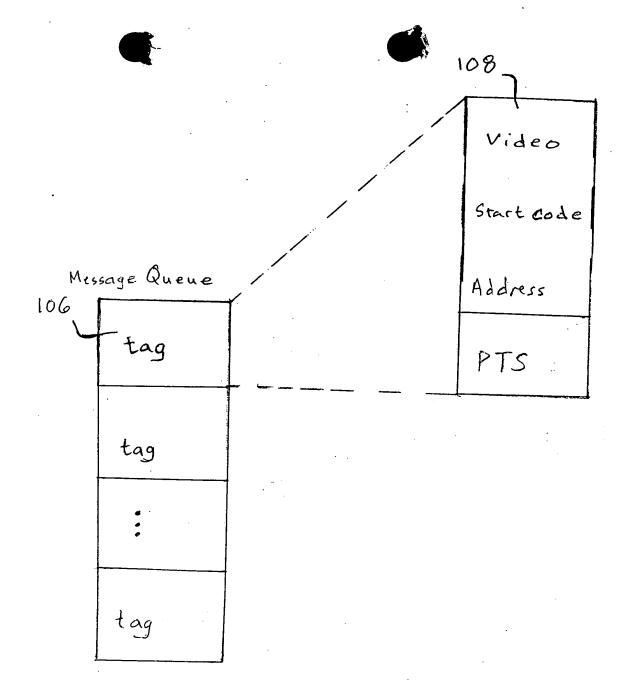


Fig. 9